

ALVIN OUYANG

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alvin_ouyang@yahoo.com **US citizen**

OBJECTIVE: To obtain a challenging position with a dynamic engineering company

EDUCATION:

- 9/00 – 3/04 **University of California, Davis**
BS, Electrical Engineering (March 2004)
- 8/96 – 5/00 **Bellarmine College Preparatory, San Jose, CA**
High School Diploma (May 2000)

IMPORTANT RELATED COURSEWORK:

Electronic Circuits I&II – transistors, solid-state devices, transistor amplifiers, logic gates, frequency response, multistage and feedback amps, A-D/D-A converters, op amps, HSPICE
Signals and Systems I&II – Fourier series/transforms, communication systems, feedback systems, Z-transform analysis, digital filter design, discrete time systems, MATLAB
Analog Integrated Circuits – analog bipolar IC design, one-stage amplifiers, current sources, differential pair, cascaded amplifiers, op amp, frequency response and feedback amplifiers.

- **Project** – Designed a Gilbert Multiplier to meet specifications, performed SPICE, and verifying circuit board experiments.

Signal Communications – Signal analysis and design, Fourier series and transforms, time-sampling, convolution, and filtering; spectral density. MATLAB/SIMULINK

Communications Electronics – Electronic circuits for analog and digital communications, including oscillators, mixers, tuned amps, modulators, demodulators, and PLL's.

- **Labs** – AM, FM Transmitters and Heterodyne Receivers, AM Homodyne Receiver.

Statistical and Digital Communications – random process models of analog and digital modulated signals and noise, and analysis of receiver performance. MATLAB/SIMULINK

WORK EXPERIENCE:

- 6/02 – 7/02 **Pulsic, Santa Clara, CA, Software Engineering Intern** - Researched design paths, performed layouts, and ran simulations using HSPICE.
- 8/01 – 9/01 **Pearson Education, Sunnyvale, CA, Marketing Assistant** - Designed MS PowerPoint presentations and MS Excel spreadsheets.
- 7/01 – 8/01 **Computer Curriculum Corporated, Sunnyvale, CA, Data Management Supervisor** – Supervised and performed data relocation of company from Sunnyvale, CA to Austin, TX.
- 4/00 – 9/00 **DMEL INC, San Jose, CA, Sales/Marketing Intern** – Designed complete datasheets for presentation using Adobe Framemaker and Adobe Illustrator. Introduced to basic logic functions and CAD tools.

SKILLS:

- Hard Working, Well Organized, Highly Motivated, Quick Learner, Very Personable
- Proficient in MS Office, Windows NT 4.0, XP, 2000, 98/95, 3.1, DOS, UNIX, Linux, Matlab, HSPICE, Altera Max Plus, Adobe Framemaker/Illustrator/Photoshop, JAVA, Power Supply, Function Generators, Oscilloscopes, Spectrum Analyzer, Pulse Generator
- Knowledgeable in C/C++, HTML, MIPS Assembly Language, WinDLX, Prolog